

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: 7,102,423  
Inventor: Seung-Hoon LEE, et al.  
Issued: September 5, 2006  
Ser. No.: 10/732,826 Filed: December 9, 2003  
Group Art Unit: 2816 Examiner: Terry D. Cunningham  
For: VOLTAGE BOOSTING CIRCUIT AND METHOD OF GENERATING  
BOOSTING VOLTAGE, CAPABLE OF ALLEVIATING EFFECTS OF  
HIGH VOLTAGE STRESS

Commissioner for Patents  
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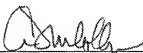
MINOR CORRECTIONS LETTER

In checking the subject original Letters Patent with our copy of the application, the following errors which occurred in the printing of the patent were noted:

Column 1, line 41, the words "the art." should read -- the prior art. --.

Since these are minor errors, we are not requesting the issuance of a Certificate of Correction. However, it is requested that this letter be made of record in the file of the patent.

Respectfully submitted,  
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